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**AMERICAN INTERNATIONAL UNIVERSITY–BANGLADESH (AIUB)**

**DEPARTMENT OF ELECTRICAL AND ELECTRONIC ENGINEERING**

**DIGITAL LOGIC AND CIRCUITS LABORATORY**

**FALL 2023-2024**

**Section: Q, Group: 2, Exp No: 07**

**LAB REPORT ON**

**Implementation of Asynchronous and synchronous counters using flip-flops**

**Supervised By**

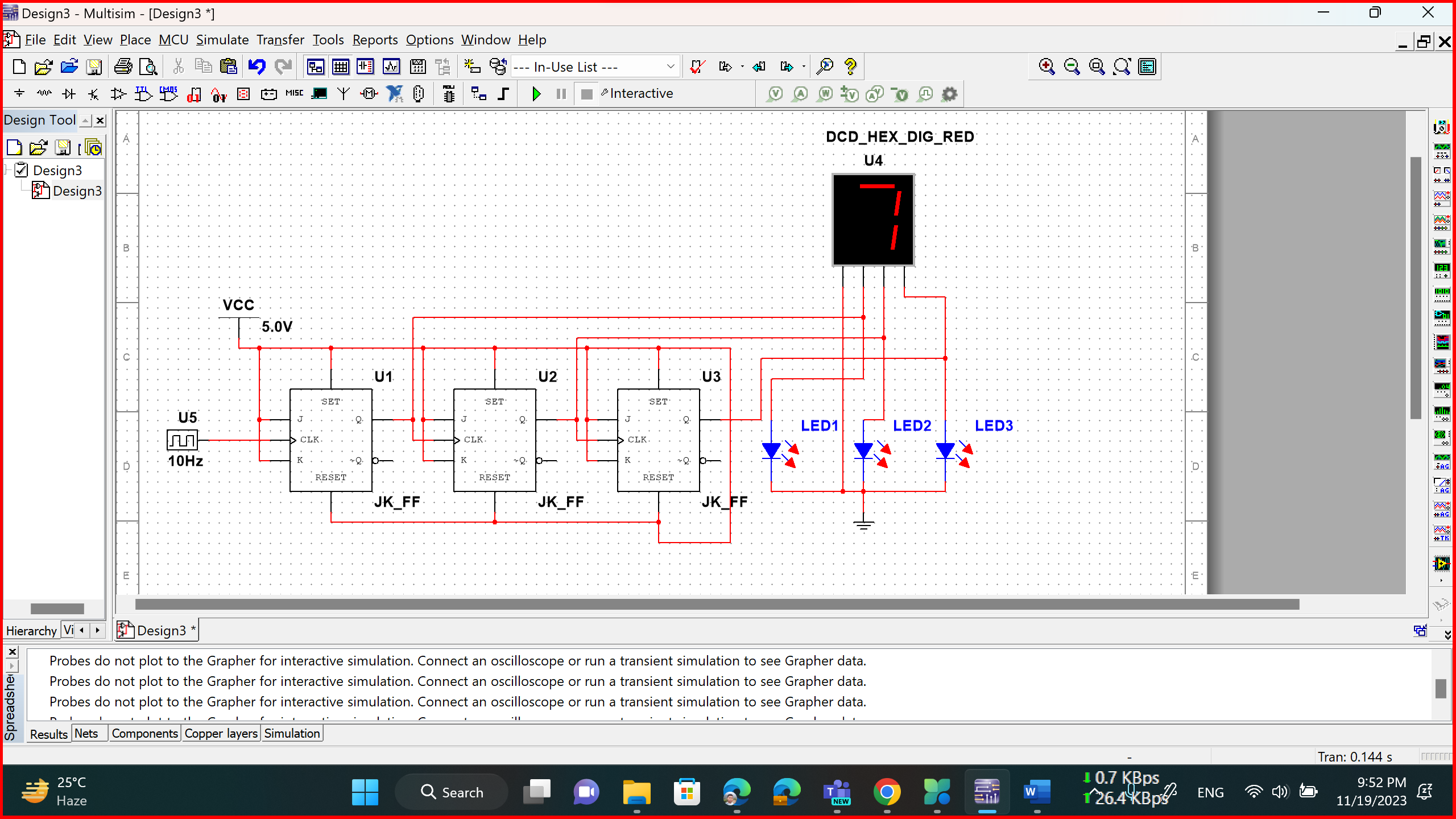
**DR. TANBIR IBNE ANOWAR**

**Submitted By:**

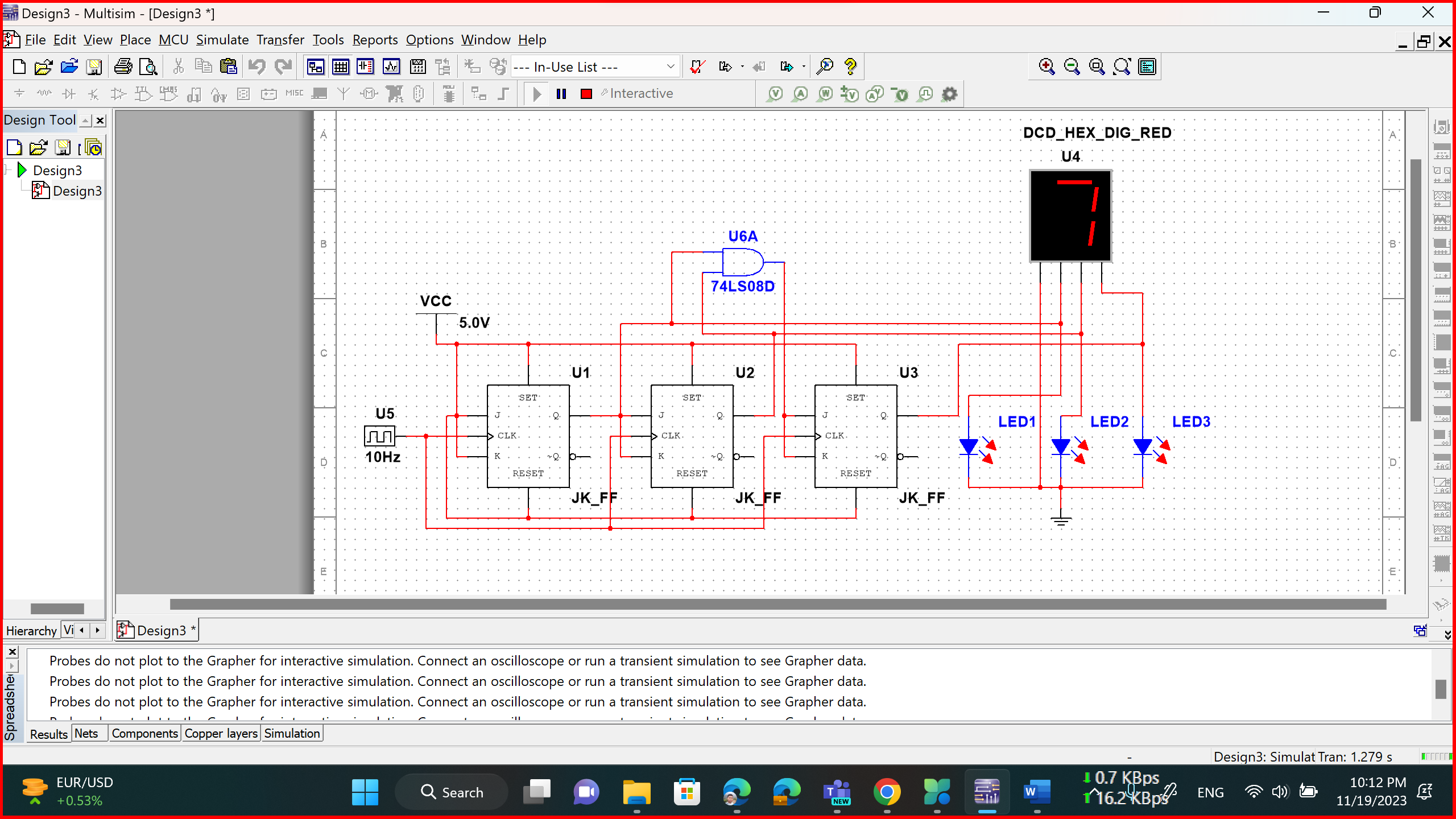
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**Simulations:**



**Figure:** 3-bit Asynchronous up Counter



**Figure:** 3-bit Synchronous up Counter